(19) World Intellectual Property Organization International Bureau



. | 1884 | 1884 | 1884 | 1884 | 1884 | 1884 | 1884 | 1884 | 1884 | 1884 | 1884 | 1884 | 1884 | 1884 | 1884 | 1

(43) International Publication Date 21 March 2002 (21.03.2002)

PCT

(10) International Publication Number WO 02/23357 A2

(51) International Patent Classification7:

(21) International Application Number: PCT/US01/28311

(22) International Filing Date:

10 September 2001 (10.09.2001)

(25) Filing Language:

English

G06F 13/38

(26) Publication Language:

English

(30) Priority Data:

09/661,748

14 September 2000 (14.09.2000) US

(71) Applicant: STMICROELECTRONICS [US/US]; 1310 Electronics Drive, Carrollton, TX 75006 (US).

- (71) Applicant (for FR only): SCHLUMBERGER SYSTEMES SA [FR/FR]; 50, Avenue Jean-Jaures, F-92120 Montrouge (FR).
- (71) Applicant (for all designated States except FR): SCHLUMBERGER TECHNOLOGIES OPERATING LTD. [GB/GB]; Brewley Building, P.O. Box 3359, BVI Road Town, Tortola (VG).
- (72) Inventors: LEAMING, Taylor, J.; 3217 Silkgrass Bend, Austin, TX 78748 (US). LEYDIER, Robert, Antoine; La

Bastide, Quartier les Pichaud, F-83250 La Londe les Maures (FR).

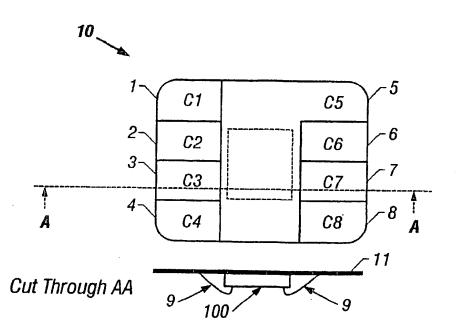
- (74) Agent: RUSSELL, Douglas, D.; Taylor Russell & Russell, P.C., 4807 Spicewood Springs Road, Building One, Suite 1200, Austin, TX 78759-8444 (US).
- (81) Designated States (national): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, PH, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, TZ, UA, UG, UZ, VN, YU, ZA, ZW.
- (84) Designated States (regional): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:

 without international search report and to be republished upon receipt of that report

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: METHOD AND DEVICE FOR UNIVERSAL SERIAL BUS SMART CARD TRAFFIC SIGNALING



(57) Abstract: A method and device are disclosed for detecting successful transfers between a Universal Serial Bus (USB) port and a USB smart card and generating a signal that provides an indication of the USB transaction activity. This USB transaction activity signal is modulated according to the USB transaction activity and drives a Light Emitting Diode (LED) in a preferred embodiment of the invention. A counter internal to the USB smart card scales the transaction activity signal such that it is perceptible to the user. Because the current through the LED depends upon the USB transaction activity, the brightness of the LED varies according to the USB transaction activity. The LED may be driven from a current mirror sink or source, or a current switch sink or source.

WO 02/23357 A2

WO 02/23357 PCT/US01/28311 4

Method and Device for Universal Serial Bus Smart Card Traffic Signaling

5

10

15

20

BACKGROUND

The invention relates generally to Integrated Circuit (IC) cards or smart cards used in processing transactions involving goods and services. Smart cards are plastic cards having microprocessor and memory circuits attached to the front or back side that connect to electrical contacts located on a front side of the card. The circuits are activated and data accessed from the card by inserting the card into a reader device that makes connections to the electrical contacts. More particularly, the invention relates to a device and method for connecting a smart card having a Universal Serial Bus (USB) interface to smart card reader devices that require no active electronic circuitry. Furthermore, the invention relates to a novel method and device for providing visual indication of data flow activity from the smart card.

Smart cards are a class of data cards. Data cards used in processing transactions are either passive or active in nature. Passive data cards include traditional credit, debit and ATM cards that make use of stored data on a magnetic strip on the back of the card. When a transaction is processed using a passive data card, transaction verification is generally required via a reader device connected to a remote computer over a telephone network. During a transaction, data may be written and read from the magnetic strip. Active data cards or smart cards make use of processor and memory circuits embedded on the card that are activated when the card is connected to a reader device. Since smart cards may contain the intelligence required to complete a transaction, the transaction may be completed

locally without resorting to a telephone connection to a remote transaction verification facility. In addition to storing data related to the owner's account such as identification number and account balance, the circuits also contain encryption for security purposes. Smart cards are used in many applications, including Subscriber Identification Module (SIM) in Global System for Mobile (GSM) telephones, TV satellite receivers, banking, health care programs, parking and highway toll payment, etc. Smart cards are expected to find increasingly wider application, eventually replacing magnetic strip type data cards.

The basic smart card standard is the International Standard ISO 7816, which provides detailed requirements for the physical, electrical, mechanical, and application programming interface for IC cards with contacts. In particular, International Standard ISO 7816-1 Physical Characteristics, International Standard 7816-2 Dimension and Location of the Contacts, International Standard ISO 7816-3 Electronic Signals and Transmission Protocols, and International Standard ISO 7816-10 Electronic Signals and Answer to Reset for Synchronous Cards are incorporated herein by reference. This standard provides for a serial interface connection to the smart card. In a great majority of cases, these cards are used in a reader connected to a computer. The reader for smart cards that comply with the ISO standard contains electronic circuits that enable communication between the card and the computer. The smart card is inserted into a card reader connector that allows a card reader first interface to communicate with the smart card. A second card reader interface is connected to a computer by means of a serial port link, a parallel port link, or a Universal Serial Bus (USB). The smart card reader based on the ISO standard makes use of a microcontroller with its associated software. Electronic components perform the task of translating signals between the smart card and the computer. Some of these components are

5

10

15

dedicated to the visualization of the data flow between the application running on the computer and the smart card.

The ISO 7816-3 class B operating conditions make use of the RESET (RST), clock (CLK) and input/output (I/O) communication signals. A smart card reader generates signals RST, CLK and merely buffers the half-duplex communication link on I/O. The signals VCC, GND, RST, CLK and I/O are connected to smart card module contacts. The ISO 7816 smart card reader provides for communication between the computer-based customer application and the smart card by means of an electronic interface circuitry. This interface drives a device that provides a visual indication of I/O activity in the link. Generally, a Light-Emitting Diode (LED) is used for visual indication.

10

5

15

20

DNEDOCID: -WO

022225762 1 -

The Universal Serial Bus (USB) has recently become firmly established and has gained wide acceptance in the Personal Computer (PC) marketplace. The USB was developed in response to a need for a standard interface that extends the concept of "plug and play" to devices external to a PC, and enables users to install and remove external peripheral devices without having to open the PC case or to remove power from the PC. The USB provides a low-cost, half-duplex serial interface that is easy to use and readily expandable. The USB also supplies up to 500 mA of current at 5 volts to interconnected devices. The USB is currently defined by the Universal Serial Bus Specification written and controlled by USB Implementers Forum, Inc., a non-profit corporation founded by the group of companies that developed the USB Specification. In particular, Chapter 5 USB Data Flow Model, Chapter 7 Electrical, and Chapter 8 Protocol Layer of Universal Serial Bus Specification are incorporated herein by reference. The increasingly widespread use of the USB in computers has led smart card reader manufacturers to develop USB interfaces for connection of their products to computers to complement the existing serial and parallel

interfaces. However, because of the differences between the serial interface defined by ISO 7816 and the serial interface defined by the USB specification, smart cards have not been directly compatible with the USB specification. And different card reader configurations have been required due to incompatibility constraints between the various computer interface standards.

The widespread use of the USB in computers has led smart card and smart card reader manufacturers to further develop their products and further reduce costs. A USB smart card reader establishes a communication link between a computer-based application and a smart card or plug module. The USB smart card reader or the USB token reader provide contacts for the interconnections between the USB signals and the four corresponding contact zones of a smart card module fitted out with an IC. The USB smart card reader connects smart card module contacts C1 and C5 as well as pins C4 and C8 respectively to the USB signals VBUS, GND, D + and D -. There is no electronic circuitry that enables the activity of the link to be reported. The smart card reader complexity has been transferred into the computer, reducing the overall costs. The USB smart card needs a pass-through connector and a driver to communicate with the computer. This architecture reduces the number of electronic components and consequently the cost without compromising the data transfer speed and the transfer reliability. There is only one IC on a USB smart card to perform communications, computing and storage.

For the foregoing reasons, there is a need to provide a smart card with a USB interface that enables a smart card to connect to an USB port without the need for any interposing electronic circuitry, enabling application software in a computer to communicate through a specific smart card software driver directly with the USB interface

5

10

15

on the smart card. There is a further need to provide a visual indication of successful data transfers between a computer and an IC module positioned on a smart card.

SUMMARY

The present invention is directed towards a method and device for providing a smart card with the capability of supporting the serial interface defined by the USB specification. It relates to a physical link between a USB port and a smart card.

Furthermore, the present invention is also directed towards a method and device for connecting a smart card to a USB port with a simple connector without the need for any interposing electronic circuitry. The invention further relates to a device and method to visualize data flow to and from a smart card.

A communication pipe is established between the client software in a computer and USB smart card endpoints. A USB smart card serial engine captures the downstream traffic and delivers it to a smart card micro-controller. The USB smart card serial engine broadcasts the upstream traffic from the smart card micro-controller to the computer. The upstream and the downstream traffic are composed of packets. Every time a packet transfer occurs without error, the USB smart card serial engine hardware generates a Correct TRansfer (CTR) flag that results in an interrupt. The USB smart card software clears this flag once the interrupt has been serviced. The CTR signal drives a first stage of a ten-bit counter. One output is selected for driving an I/O buffer connected to an I/O connector pin. This pin is connected to an LED on a smart card reader that blinks at a pace set by the USB traffic. All functions are included in a single IC, and the only electronic components required in the smart card reader are a LED to provide visual indication of data flow activity, the passthrough connector and in some embodiment a resistor limiting the current

5

10

15

WO⁻02/23357 PCT/US01/28311

in the LED. The present invention provides a method and device that allows USB traffic between a host computer and a USB smart card to be visualized.

A method having features of the present invention comprises transmitting and receiving USB packets comprising differential serial signals between the smart card module and the USB port, generating a correct transfer signal by the smart card module upon successful transmission and reception of a USB packet and signaling transaction activity based on the correct transfer signal. The signaling step may include connecting the correct transfer signal to an input of a counter, both positioned within the smart card module, and signaling transaction activity based on an output of the counter. The method may further include selecting an output of the counter so that the user is provided with a perceptible indication of signaling transaction activity. It may also include connecting the output of the counter to an output buffer positioned within the smart card module that drives a LED. The output buffer may comprise a current source circuit connected to a GND reference voltage. The output buffer may comprise a switch circuit connected to a VBUS supply voltage. The output buffer may comprise a switch circuit connected to a GND reference voltage.

Another embodiment of the present invention may comprise transmitting and receiving USB packets comprising differential serial signals between the USB port and external terminals of transceivers positioned within the smart card module, connecting internal terminals of the transceivers to inputs of a serial engine positioned within the smart card module, interconnecting a correct transfer signal from an output of the serial engine to a micro-controller and a counter, both positioned within the smart card module, controlling the counter from the micro-controller, driving an input of an output buffer positioned

5

10

15

within the smart card module from an output of the counter, and activating a device from an output of the output buffer for signaling transaction activity based on the correct transfer signal. The device being activated may be a LED, having an anode and a cathode.

The method may further include connecting the anode of the LED to a VBUS supply voltage of the smart card module, connecting the cathode of the LED to an I/O contact of the smart card module, connecting a second NMOS transistor of a current mirror circuit within the smart card module between the I/O contact and a GND reference voltage contact for providing a modulated current sink for illumination of the LED, connecting a first NMOS transistor of the current mirror circuit within the smart card module to the second NMOS transistor, and providing a modulated current to the first NMOS transistor from the output of the output buffer that reflects the USB transaction activity, causing the illumination intensity of the LED to be modulated according to the USB transaction activity. The cathode of the LED may be connected to a contact of the smart card module selected from the group consisting of C2, C3, C6 and C7.

The method may further include connecting the anode of the LED to an I/O contact of the smart card module, connecting the cathode of the LED to a GND reference voltage of the smart card module, connecting a second PMOS transistor of a current mirror circuit within the smart card module between the I/O contact and a VBUS supply voltage contact for providing a modulated current sink for illumination of the LED, connecting a first PMOS transistor of the current mirror circuit within the smart card module to the second PMOS transistor, and providing a modulated current to the first PMOS transistor from the output of the output buffer that reflects the USB transaction activity, causing the illumination intensity of the LED to be modulated according to the USB transaction

5

10

15

activity. The method may include the anode of the LED being connected to a contact of the smart card module selected from the group consisting of C2, C3, C6 and C7.

The method may further include connecting the cathode of the LED to a GND reference voltage of the smart card module, connecting a first terminal of a resistor to the anode of the LED, connecting a second terminal of the resistor to an I/O contact of the smart card module, connecting a PMOS transistor switch of a current switch circuit within the smart card module between the I/O contact and a VBUS supply voltage contact, and providing a modulated voltage source from the output of the output buffer that reflects the USB transaction activity to a gate terminal of the PMOS transistor switch within the smart card module, causing the illumination intensity of the LED to be modulated according to the USB transaction activity. The second terminal of the resistor may be connected to a contact of the smart card module selected from the group consisting of C2, C3, C6 and C7.

The method may further comprise connecting the anode of the LED to a VBUS supply voltage of the smart card module, connecting a first terminal of a resistor to the cathode of the LED, connecting a second terminal of the resistor to an I/O contact of the smart card module, connecting an NMOS transistor switch of a current switch circuit within the smart card module between the I/O contact and a GND reference voltage contact, and providing a modulated voltage source from the output of the output buffer that reflects the USB transaction activity to a gate terminal of the NMOS transistor switch within the smart card module, causing the illumination intensity of the LED to be modulated according to the USB transaction activity. The second terminal of the resistor may be connected to a contact of the smart card module selected from the group consisting of C2, C3, C6 and C7.

5

10

15

A further embodiment of the present invention is a device comprising transceivers positioned within the smart card module for transmitting and receiving USB packets comprising differential serial signals between the USB port and external terminals of transceivers, the transceivers having internal terminals connected to inputs of a serial engine positioned within the smart card module, a correct transfer signal from an output of the serial engine interconnected to a micro-controller and a counter, both positioned within the smart card module, the counter being controlled from the micro-controller, an output of the counter driving an input of an output buffer positioned within the smart card module, and a device activated from an output of the output buffer for signaling transaction activity based on the correct transfer signal. The device being activated may be a LED, having an anode and a cathode.

5

10

15

20

The device may further include the anode of the LED connected to a VBUS supply voltage of the smart card module, the cathode of the LED connected to an I/O contact of the smart card module, a second NMOS transistor of a current mirror circuit within the smart card module connected between the I/O contact and a GND reference voltage contact for providing a modulated current sink for illumination of the LED, a first NMOS transistor of the current mirror circuit within the smart card module connected to the second NMOS transistor, and a modulated current provided to the first NMOS transistor from the output of the output buffer that reflects the USB transaction activity, causing the illumination intensity of the LED to be modulated according to the USB transaction activity. The cathode of the LED may be connected to a contact of the smart card module selected from the group consisting of C2, C3, C6 and C7.

The device may further comprise the anode of the LED connected to an I/O contact of the smart card module, the cathode of the LED connected to a GND reference voltage of

the smart card module, a second PMOS transistor of a current mirror circuit within the smart card module connected between the I/O contact and a VBUS supply voltage contact for providing a modulated current sink for illumination of the LED, a first PMOS transistor of the current mirror circuit within the smart card module connected to the second PMOS transistor, and a modulated current provided to the first PMOS transistor from the output of the output buffer that reflects the USB transaction activity, causing the illumination intensity of the LED to be modulated according to the USB transaction activity. The anode of the LED is connected to a contact of the smart card module selected from the group consisting of C2, C3, C6 and C7.

The device may further comprise the cathode of the LED connected to a GND reference voltage of the smart card module, a first terminal of a resistor connected to the anode of the LED, a second terminal of the resistor connected to an I/O contact of the smart card module, a PMOS transistor switch of a current switch circuit within the smart card module connected between the I/O contact and a VBUS supply voltage contact, and a modulated voltage source provided from the output of the output buffer that reflects the USB transaction activity to a gate terminal of the PMOS transistor switch within the smart card module, causing the illumination intensity of the LED to be modulated according to the USB transaction activity. The second terminal of the resistor is connected to a contact of the smart card module selected from the group consisting of C2, C3, C6 and C7.

The device may further include the anode of the LED connected to a VBUS supply voltage of the smart card module, a first terminal of a resistor connected to the cathode of the LED, a second terminal of the resistor connected to an I/O contact of the smart card module, an NMOS transistor switch of a current switch circuit within the smart card module connected between the I/O contact and a GND reference voltage contact, and a

10

15

modulated voltage source provided from the output of the output buffer that reflects the USB transaction activity to a gate terminal of the NMOS transistor switch within the smart card module, causing the illumination intensity of the LED to be modulated according to the USB transaction activity. The second terminal of the resistor may be connected to a contact of the smart card module selected from the group consisting of C2, C3, C6 and C7.

BRIEF DESCRIPTION OF THE DRAWINGS

These and other features, aspects, and advantages of the present invention will become understood with regard to the following description, appended claims, and accompanying drawings where:

- FIG. 1 shows a smart card module that is compatible with a USB smart card for connecting to a USB serial link;
- FIG. 2 shows a block diagram of an ISO standard smart card reader and a smart card;
 FIG. 3 shows a block diagram of a novel IC having USB transceivers and USB traffic signaling circuits;
- FIG. 4 shows a USB smart card reader and a smart card;
- FIG. 5 shows a USB interface connection between a USB smart card module and a USB port using a cable and driving a LED to indicate USB traffic activity;
- FIG. 6 shows a USB smart card module plug ready to be separated from a USB smart card;

 FIG. 7 shows a USB style token reader with a USB smart card module plug inserted;

 FIG. 8 shows a USB interface connection between a USB smart card module within a

 token reader and a USB port using a plug connector and driving a LED to indicate USB

traffic activity;

00003E7A0 | 1

5

10

15

FIG. 9 shows the electrical schematic of the USB traffic signaling LED biased by a current sink to GND reference voltage;

- FIG. 10 shows the electrical schematic of the USB traffic signaling LED biased by a current source from VBUS;
- 5 FIG. 11 shows the electrical schematic of the USB traffic signaling LED enabled by a switch to VBUS; and

FIG. 12 shows the electrical schematic of the USB traffic signaling LED enabled by a switch to GND reference voltage.

10

15

20

DETAILED DESCRIPTION

Turning to FIG. 1, FIG. 1 shows a smart card module 10 that is compatible with ISO7816-2 Dimension and Location of the Contacts. The smart card module 10 has eight electrical contacts 1 - 8 positioned on a substrate 11 and an IC 100 attached to the side of the substrate 11 opposite the contacts 1 - 8. The electrical contacts 1 - 8 are electrically isolated from each other. Electrical connections between the IC 100 and the contacts 1 - 8 are accomplished through the use of bonding wires 9. Electrical connections between the opposite sides of the substrate 11 may be accomplished by any means common in the art, including conductive vias. The IC 100 and the bonding wires 9 are normally encapsulated for protection from mechanical and environmental effects. The smart card module 10 is compatible for use in asynchronous ISO 7816 applications. Contact C1 1 is assigned to the Supply voltage VCC, contact C2 2 is assigned to the Reset signal RST, contact C3 3 is assigned to the Clock signal CLK, contact C5 5 is assigned to the GND reference voltage, contact C6 6 is assigned to the Variable supply voltage VPP 6 and contact C7 7 is assigned to Data input/output I/O. In synchronous ISO7816-10 applications contact C4 4 is assigned

WO 02/23357 PCT/US01/28311 .

to function code FCB and contact C8 8 may also be used for other synchronous applications. The smart card module 10 is compatible for use in USB applications. In a USB smart card application. Contact C1 1 is assigned to the Supply voltage VBUS, contact C4 4 is assigned to the Data line D+, contact C5 5 is assigned to the GND reference voltage, contact C8 8 is assigned to the Data line D -. Contacts C2 2, C3 3, C6 6 and C7 7 are available for other uses. The present invention consists of the use of one contact of C2, C3, C6 and C7 module contacts to report the USB activities between the computer-based application and the USB smart card IC 100.

Turning to FIG. 2, FIG. 2 shows a block diagram of an ISO standard smart card reader 20. The smart card reader 20 is connected to a computer 21. A smart card 200 is inserted in the smart card reader 20. The protocol used between the smart card 200 and the smart card reader 20 is described by the ISO7816 standard. This protocol is not embedded in the computer 21. When the smart card 200 is inserted in the smart card reader 20, a smart card module 10 is connected to a smart card first interface 26 using a pass-through connector 27. The first interface 26 masters the half-duplex ISO7816 protocol. The smart card reader 20 is connected to the computer parallel port 22, serial port 23 or even a USB port 24 through a second interface 25. An LED 300, driven by hardware circuitry in the smart card reader 20, provides an indication of any traffic between the smart card and the computer.

Turning now to FIG. 3, FIG. 3 shows a block diagram of a novel IC 100 having USB transceivers 36 and USB traffic signaling circuits 31, 32. FIG. 3 depicts one particular embodiment of the present invention. The USB transceiver 36 receives differential serial signals D + and D -, and communicates with a serial engine 35. The serial engine 35 exchanges data with a micro-controller 34. A software program is stored in a memory 33.

5

10

15

Every time a USB packet is transferred upstream or downstream with success, a Correct Transfer (CTR) signal 37, connected between the serial engine 35, the micro-controller 34 and a counter 32, is set by the serial engine hardware 35. A successful transfer occurs when no error is detected in a packet transfer. The software program stored in the memory 33 causes the micro-controller 34 to clear the CTR signal 37 once a CTR interrupt is serviced by the micro-controller 34. The counter 32 has, for example, ten stages. The software program stored in the memory 33 causes the micro-controller 34 to select an appropriate output of the counter 32 via the select signal 38. The selected output of the counter 32 activates the LED driver 31, connected to the I/O pad 39.

Turning now to FIG. 4, FIG. 4 shows a smart card reader 40 and a smart card 200. The smart card 200 contains a smart card module 10 as described earlier. The smart card 200 plugs into the smart card reader 40. The smart card reader 40 has connecting contacts of a pass-through connector 27 that connect the smart card module contacts to a USB cable 42 terminated by a USB series A plug 41. The USB series A plug connects to a USB Hub port 24 on a Host PC 21 or other USB hub. A LED 300 is mounted on the USB card reader in addition to the pass-through connector 27. No other active components are required in the USB smart card reader. The USB compatible smart card 200 inserted in the USB style smart card reader 40 terminated by the USB series A plug connector 41 constitute a USB smart card device 43.

Turning now to FIG. 5, FIG. 5 shows a USB interface connection between a USB smart card module 10 and a USB port 24 using a cable 42 and driving a LED 300 to indicate USB traffic activity, as physically depicted in FIG. 4. Eight pass-through connector pins 50 within the smart card reader 40 connect to the contacts on the smart card module 10. The electrical connections from the smart card module are carried via the cable

5

10

15

WO 02/23357 PCT/US01/28311 .

5

10

15

PARDOCIO: -WO

022335742 1 >

42 to a USB Series A plug connector 41. The USB compatible smart card module 10 is inserted in the USB style smart card reader 40 equipped with the USB cable 42 terminated by the USB series A plug connector 41. The cable 42 utilizes four wires for connecting the smart card module 10 to the plug connector 41. The connector 41 may plug directly into a USB port 24 of a Host PC 21 root hub equipped with a Series A receptacle or a USB port of a hub equipped with the same receptacle as depicted in FIG. 4. The hub provides the VBUS power supply connected to the contact C1 1 on the smart card module 10, the GND reference voltage connected to the contact C5 5 on the smart card module 10, the Data line D + connected to the contact C4 4 and the Data line D - connected to the contact C8 8. In the present embodiment of the invention, when a smart card is connected to a USB interface, the smart card module contacts C2, C3, and C6 are not assigned. The anode of a LED 300 is connected to the contact C7 7 of the smart card module 10 and the cathode of the LED 300 is connected to the GND reference voltage of the smart card module 10. A USB style card reader will normally provide these signals to the smart card module according to the electrical and transmission protocols defined in the USB specification. While the present embodiment of the invention makes use of contact C4 4 and contact C8 8, the ISO 7816-2standard reserves these two contacts for future use. The ISO 7816-10 assigns C4 to Function code FCB in synchronous application.

Turning now to FIG. 6, FIG. 6 shows a USB smart card module plug 400 ready to

20 be separated from a USB smart card 200. A smart card module 10 is positioned on the

smart card module plug 400. This is another form factor of a USB smart card 200. When

the smart card module plug 400 is separated from the smart card 200, it may be inserted

into a USB token reader 500 as shown in FIG. 7.

Turning to FIG. 7, FIG. 7 shows a token reader 500 with a plug module 400 inserted. This type of module plug 400 is widely used in SIM applications designed for GSM telephones. The connector 41 may plug directly into a USB port 24 of a Host PC 21 root hub equipped with a Series A receptacle or a USB port of a hub equipped with the same receptacle, as depicted in FIG. 4. The electrical configuration of the smart card module 10 positioned on the smart card module plug 400 in the token reader 500 is the same as that depicted in FIG. 5, except that the plug connector 41 is attached to the token reader 500 rather than the cable 42. The USB compatible smart card module plug 400 inserted in the USB style smart card reader 500 terminated by the USB series A plug connector 41 constitute a USB smart card device 46.

Turning now to FIG. 8, FIG. 8 shows a USB interface connection between a USB smart card module 10 within a token reader 500 and a USB port 24 using a plug connector 41 and driving a LED 300 to indicate USB traffic activity, as physically depicted in FIG. 7. Eight pass-through connector pins 50 within the token reader 500 connect to the contacts on the smart card module 10. The electrical connections from the smart card module 10 are connected to a USB Series A plug connector 41. The USB compatible smart card module 10 is inserted in a USB style token reader 500 terminated by the USB series A plug connector 41. The connector 41 may plug directly into a USB port 24 of a Host PC 21 root hub equipped with a series A receptacle or a USB port of a hub equipped with the same receptacle as depicted in FIG.4. The hub provides the VBUS power supply connected to the contact C1 1 on the smart card module 10, the GND reference voltage connected to the contact C5 5 on the smart card module 10, the Data signal D + connected to the contact C4 4 on the smart card module 10, and the Data signal D - connected to the contact C8 8 on the smart card module 10. The anode of a LED 300 is connected to the contact C7 of the

5

10

15

WO 02/23357 PCT/US01/28311 .

smart card module 10 and the cathode of the LED is connected to the GND reference voltage of the smart card module 10. A USB style token reader will normally provide these signals to the smart card module according to the electrical and transmission protocols defined in the USB specification. In the present embodiment of the invention the smart card module contacts C2, C3, and C6 are not assigned. While the present embodiment of the invention makes use of contact C4 4 and contact C8 8, the ISO 7816-2 standard reserves these two contacts for future use. The ISO 7816-10 assigns C4 to Function code FCB in synchronous application.

5

10

15

20

000005780 1 5

Turning now to FIG. 9, FIG. 9 shows the electrical schematic 60 of the USB traffic signaling LED 300 biased by a current source to contact C5 5. The current source circuit is part of the IC 100 mounted on the smart card module 10. FIG. 9 depicts a current mirror circuit 60 that comprises NMOS transistors 63, 64 that enable the current in the LED 300 to be determined by the current 62 into the NMOS transistor 63, such that the current 62 into the NMOS transistor 63 is proportional to the current through the NMOS transistor 64 that flows through the LED 300. Current mirror circuits and their operation are well known to skilled practitioners of the electronics art. The NMOS transistor 64 is connected between the contact C7 7 and the contact C5 5. The LED 300 is connected between the supply voltage VBUS and the contact C7 7, the anode of the LED 300 being connected to VBUS and the cathode of the LED 300 being connected to the contact C7 7. The current 62, and hence current through the LED 300, determines the brightness of the LED 300, and is a function of the amount of USB traffic.

Turning now to FIG. 10, FIG. 10 shows the electrical schematic 70 of the USB traffic signaling LED 300 biased by a current source from contact C1 1. The current source circuit is part of the IC 100 mounted on the smart card module 10. FIG. 10 depicts a current

mirror circuit 70 that comprises PMOS transistors 73, 74 that enable the current in the LED 300 to be determined by the current 72 into the PMOS transistor 73, such that the current 72 into the PMOS transistor 73 is proportional to the current through the PMOS transistor 74 that flows through the LED 300. Current mirror circuits and their operation are well known to skilled practitioners of the electronics art. The PMOS transistor 74 is connected between the I/O contact 7 and the contact C1 1. The LED 300 is connected between the GND reference voltage and the contact C7 7, the anode of the LED 300 being connected to the contact C7 7 and the cathode of the LED 300 being connected to GND reference voltage. The current 72, and hence current through the LED 300, determines the brightness of the LED 300, and is a function of the amount of USB traffic.

5

10

15

20

NSDOCID: <WO_

_0223357A2_I_>

Turning now to FIG. 11, FIG. 11 shows the electrical schematic 80 of the USB traffic signaling LED 300 enabled by a current switch from contact C1 1. The current switch circuit is part of the IC 100 mounted on the smart card module 10. FIG. 11 depicts a voltage switch circuit 80 comprising a PMOS transistor switch 83 connected between the contact C7 7 and the contact C1 1. A series circuit consisting of an LED 300 and a resistor 11 is connected between GND reference voltage and the contact C7 7, the cathode of the LED 300 being connected to GND reference voltage. When a negative voltage is applied to the gate 82 of the PMOS transistor 83, it is switched on, causing current to flow from the contact C7 7 through the resistor 11 and the LED 300 to the GND reference voltage. The resistor 11 limits the current through the LED 300. The modulation of the voltage at the gate 82 of the PMOS transistor 83 is a function of the USB traffic, making the brightness of the LED 300, a function of the amount of USB traffic.

Turning now to FIG. 12, FIG. 12 shows the electrical schematic 90 of the USB traffic signaling LED 300 enabled by a current switch to the contact C5 5. The current

5

10

DVIGUOCIU- 1910

switch circuit is part of the IC 100 mounted on the smart card module 10. FIG. 12 depicts a voltage switch circuit 90 comprising an NMOS transistor switch 93 connected between the contact C7 7 and the contact C5 5. A series circuit consisting of an LED 300 and a resistor 12 is connected between the supply voltage VBUS and the contact C7 7, the anode of the LED 300 being connected to VBUS. When a positive voltage is applied to the gate 92 of the NMOS transistor 93, it is switched on, causing current to flow through the LED 300 and the resistor 12 to the GND reference voltage. The resistor 12 limits the current through the LED 300. The modulation of the voltage at the gate 92 of the NMOS transistor 93 is a function of the USB traffic, making the brightness of the LED 300, a function of the amount of USB traffic. Although the present invention has been described in detail with reference to certain preferred embodiments, it should be apparent that modifications and adaptations to those embodiments may occur to persons skilled in the art without departing from the spirit and scope of the present invention as set forth in the following claims.

What is claimed is:

1 1. A method for signaling transaction activity between a Universal Serial Bus (USB)

- 2 smart card module and a USB port, comprising:
- 3 a) transmitting and receiving USB packets comprising differential serial signals
- between the smart card module and the USB port;
- b) generating a correct transfer signal by the smart card module upon successful
- 6 transmission and reception of a USB packet; and
- . 7 c) signaling transaction activity based on the correct transfer signal.
 - 1 2. A method according to claim 1, wherein the signaling step comprises:
- a) connecting the correct transfer signal to an input of a counter, both positioned
- 3 within the smart card module; and
- b) signaling transaction activity based on an output of the counter.
- 1 3. A method according to claim 2, further comprising selecting an output of the counter so
- 2 that the user is provided with a perceptible indication of signaling transaction activity.
- 1 4. A method according to claim 2, further comprising connecting the output of the
- 2 counter to an output buffer positioned within the smart card module that drives a LED.
- 1 5. A method according to claim 4, wherein the output buffer comprises a current source
- 2 circuit connected to a GND reference voltage.

1 6. A method according to claim 4, wherein the output buffer comprises a current source

- 2 circuit connected to a VBUS supply voltage.
- 1 7. A method according to claim 4, wherein the output buffer comprises a switch circuit
- 2 connected to a VBUS supply voltage.
- 1 8. A method according to claim 4, wherein the output buffer comprises a switch circuit
- 2 connected to a GND reference voltage.
- 9. A method for signaling transaction activity between a Universal Serial Bus (USB)
- 2 smart card module and a USB port, comprising:
- a) transmitting and receiving USB packets comprising differential serial signals
- 4 between the USB port and external terminals of transceivers positioned within the
- 5 smart card module;
- b) connecting internal terminals of the transceivers to inputs of a serial engine
- 7 positioned within the smart card module;
- 8 c) interconnecting a correct transfer signal from an output of the serial engine to a
- 9 micro-controller and a counter, both positioned within the smart card module;
- d) controlling the counter from the micro-controller;
- e) driving an input of an output buffer positioned within the smart card module from
- 12 an output of the counter; and
- 13 f) activating a device from an output of the output buffer for signaling transaction
- activity based on the correct transfer signal.

1 10. A method according to claim 9, wherein device being activated is a LED, having an

- 2 anode and a cathode.
- 1 11. A method according to claim 10, further comprising the steps of:
- 2 a) connecting the anode of the LED to a VBUS supply voltage
- b) connecting the cathode of the LED to an I/O contact of the smart card module;
- 4 c) connecting a second NMOS transistor of a current mirror circuit within the smart
- 5 card module between the I/O contact and a GND reference voltage contact for
- 6 providing a modulated current sink for illumination of the LED;
- a) connecting a first NMOS transistor of the current mirror circuit within the smart
- 8 card module to the second NMOS transistor; and
- b) providing a modulated current to the first NMOS transistor from the output of the
- output buffer that reflects the USB transaction activity, causing the illumination
- intensity of the LED to be modulated according to the USB transaction activity.
 - 1 12. A method according to claim 11, wherein the cathode of the LED is connected to a
- 2 contact of the smart card module selected from the group consisting of C2, C3, C6 and
- 3 C7.

022335742 1 -

- 1 13. A method according to claim 10, further comprising the steps of:
- a) connecting the anode of the LED to an I/O contact of the smart card module;
- 3 b) connecting the cathode of the LED to a GND reference voltage

WO 02/23357 PCT/US01/28311 .

1	c)	connecting a second PMOS transistor of a current mirror circuit within the smart
2		card module between the I/O contact and a VBUS supply voltage contact for
3		providing a modulated current sink for illumination of the LED;
4	d)	connecting a first PMOS transistor of the current mirror circuit within the smart
5		card module to the second PMOS transistor; and
6	e)	providing a modulated current to the first PMOS transistor from the output of the
7		output buffer that reflects the USB transaction activity, causing the illumination
8		intensity of the LED to be modulated according to the USB transaction activity.
1	14. A	method according to claim 13, wherein the anode of the LED is connected to a
2	contac	t of the smart card module selected from the group consisting of C2, C3, C6 and C7.
1	15. A	method according to claim 10, further comprising the steps of:
2	a)	connecting the cathode of the LED to a GND reference voltage
3	b)	connecting a first terminal of a resistor to the anode of the LED;
4	c)	connecting a second terminal of the resistor to an I/O contact of the smart card
5		module;
6	d)	connecting a PMOS transistor switch of a current switch circuit within the smart
7		card module between the I/O contact and VBUS supply voltage contact; and
8	e)	providing a modulated voltage source from the output of the output buffer that
9		reflects the USB transaction activity to a gate terminal of the PMOS transistor
10		switch within the smart card module, causing the illumination intensity of the LED
11		to be modulated according to the USB transaction activity.

BRIEDOCIDE AND

00000E780 1 ~

1 16. A method according to claim 15, wherein the second terminal of the resistor is

- 2 connected to a contact of the smart card module selected from the group consisting of C2,
- 3 C3, C6 and C7.
- 1 17. A method according to claim 10, further comprising the steps of:
- a) connecting the anode of the LED to the VBUS supply voltage of the smart card module;
- b) connecting a first terminal of a resistor to the cathode of the LED;
- 5 c) connecting a second terminal of the resistor to an I/O contact of the smart card 6 module;
- d) connecting an NMOS transistor switch of a current switch circuit within the smart card module between the I/O contact and a GND reference voltage; and
- 9 e) providing a modulated voltage source from the output of the output buffer that
 10 reflects the USB transaction activity to a gate terminal of the NMOS transistor
 11 switch within the smart card module, causing the illumination intensity of the
 12 LED to be modulated according to the USB transaction activity.
- 1 18. A method according to claim 17, wherein the second terminal of the resistor is
- 2 connected to a contact of the smart card module selected from the group consisting of C2,
- 3 C3, C6 and C7.
- 1 19. A device for signaling transaction activity between a Universal Serial Bus (USB) smart
- 2 card module and a USB port, comprising:

3	a)	transceivers positioned within the smart card module for transmitting and receiving
4		USB packets comprising differential serial signals between the USB port and
5		external terminals of transceivers;
6	b)	the transceivers having internal terminals connected to inputs of a serial engine
7		positioned within the smart card module;
. 8	c)	a correct transfer signal from an output of the serial engine interconnected to a
9		micro-controller and a counter, both positioned within the smart card module
10	d)	the counter being controlled from the micro-controller;
11	e)	an output of the counter driving an input of an output buffer positioned within the
12		smart card module; and
13	f)	a device activated from an output of the output buffer for signaling transaction
14		activity based on the correct transfer signal.
1	20. A	device according to claim 19 wherein the device being activated is a LED, having
2	an and	de and a cathode.
1	21. A	device according to claim 20, further comprising:
2	a)	the anode of the LED connected to a VBUS supply voltage of the smart card
3		module;
4	b)	the cathode of the LED connected to an I/O contact of the smart card module;
5	c)	a second NMOS transistor of a current mirror circuit within the smart card module
6		connected between the I/O contact and a GND reference voltage contact for
7		providing a modulated current sink for illumination of the LED;

d) a first NMOS transistor of the current mirror circuit within the smart card module connected to the second NMOS transistor; and

- e) a modulated current provided to the first NMOS transistor from the output of the output buffer that reflects the USB transaction activity, causing the illumination intensity of the LED to be modulated according to the USB transaction activity.
- 1 22. A device according to claim 21, wherein the cathode of the LED is connected to a
- 2 contact of the smart card module selected from the group consisting of C2, C3, C6 and
- 3 C7.

INSDOCID: <WO__

_0223357A2_I_>

- 1 23. A device according to claim 20, further comprising:
- a) the anode of the LED connected to an I/O contact of the smart card module;
- b) the cathode of the LED connected to a GND reference voltage of the smart card
 module;
- c) a second PMOS transistor of a current mirror circuit within the smart card module connected between the I/O contact and a VBUS supply voltage contact for providing a modulated current sink for illumination of the LED:
- d) a first PMOS transistor of the current mirror circuit within the smart card module connected to the second PMOS transistor; and
- e) a modulated current provided to the first PMOS transistor from the output of the output buffer that reflects the USB transaction activity, causing the illumination intensity of the LED to be modulated according to the USB transaction activity.

1 24. A device according to claim 23, wherein the anode of the LED is connected to a contact

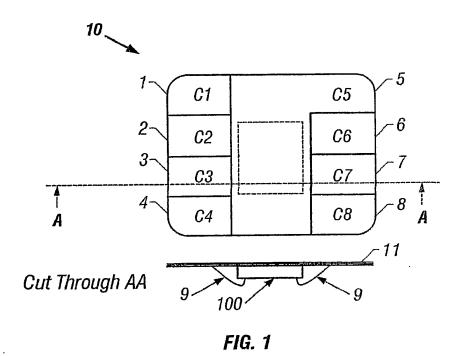
- 2 of the smart card module selected from the group consisting of C2, C3, C6 and C7.
- 1 25. A device according to claim 20, further comprising:
- a) the cathode of the LED connected to a GND reference voltage of the smart card
- 3 module;
- 4 b) a first terminal of a resistor connected to the anode of the LED;
- 5 c) a second terminal of the resistor connected to an I/O contact of the smart card
 6 module;
- d) a PMOS transistor switch of a current switch circuit within the smart card module connected between the I/O contact and a VBUS supply voltage contact; and
- e) a modulated voltage source provided from the output of the output buffer that
 reflects the USB transaction activity to a gate terminal of the PMOS transistor
 switch within the smart card module, causing the illumination intensity of the LED
 to be modulated according to the USB transaction activity.
- 1 26. A device according to claim 25, wherein the second terminal of the resistor is
- 2 connected to a contact of the smart card module selected from the group consisting of C2,
- 3 C3, C6 and C7.
- 1 27. A device according to claim 20, further comprising:
- a) the anode of the LED connected to a VBUS supply voltage of the smart card
- 3 module;
- b) a first terminal of a resistor connected to the cathode of the LED;

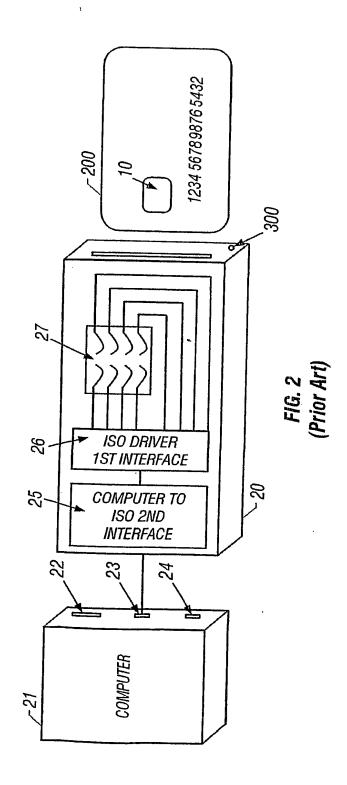
3	c)	a second terminal of the resistor connected to an I/O contact of the smart card
6		module;

- d) an NMOS transistor switch of a current switch circuit within the smart card module connected between the I/O contact and a GND reference voltage; and
- e) a modulated voltage source provided from the output of the output buffer that
 reflects the USB transaction activity to a gate terminal of the NMOS transistor
 switch within the smart card module, causing the illumination intensity of the LED
 to be modulated according to the USB transaction activity.
- 1 28. A device according to claim 27, wherein the second terminal of the resistor is
- 2 connected to a contact of the smart card module selected from the group consisting of C2,
- 3 C3, C6 and C7.

3NSDOCID: <WO

000005740 1 .





PCT/US01/28311

3/9

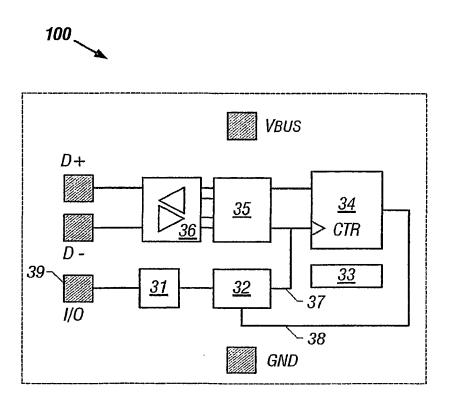
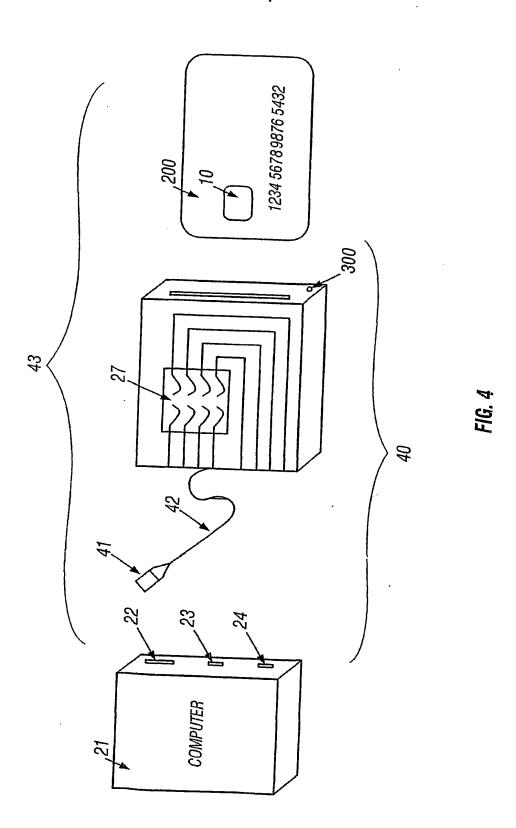


FIG. 3

DESCRIPTION OF THE PROPERTY IS

WO 02/23357

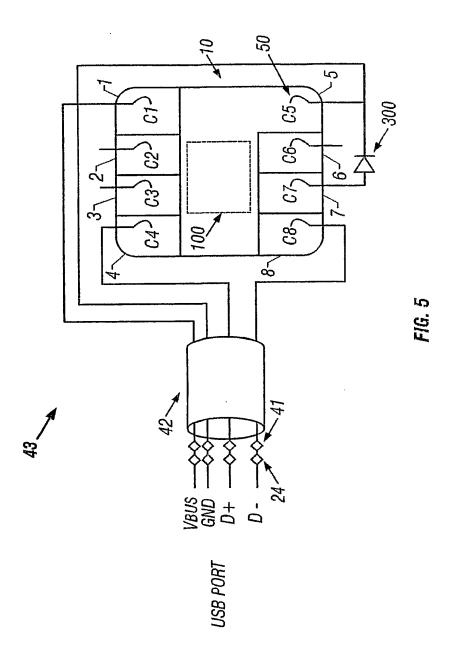




MCDUCID MAIU GOGGGETA

WO 02/23357 PCT/US01/28311 .

5/9



nunnonn atto nonnertta t

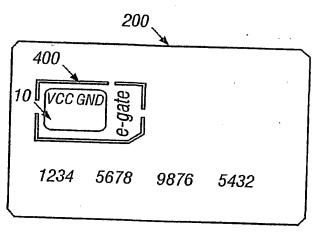
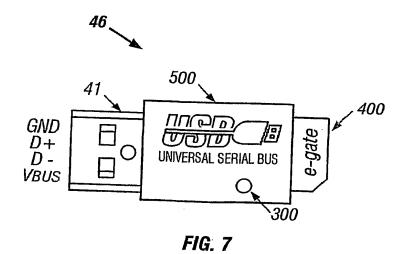


FIG. 6



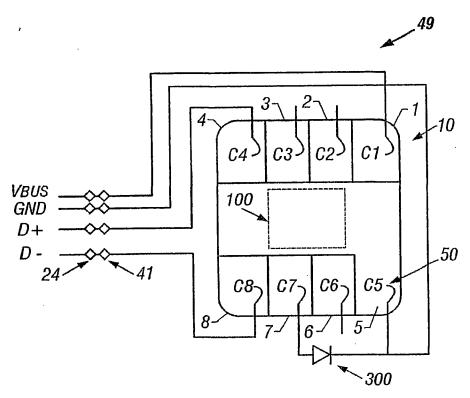


FIG. 8

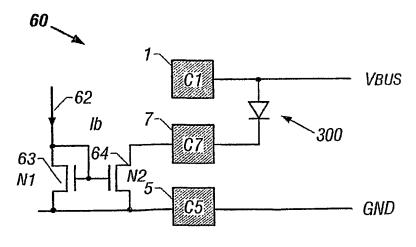


FIG. 9

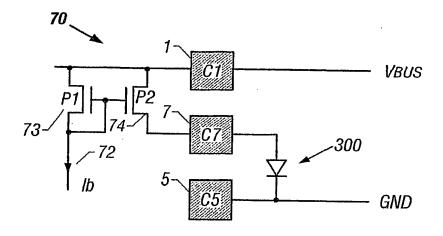


FIG. 10

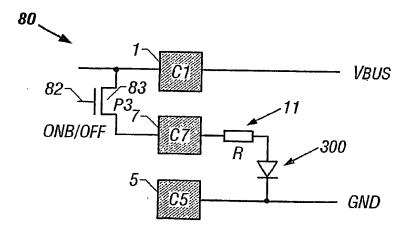


FIG. 11

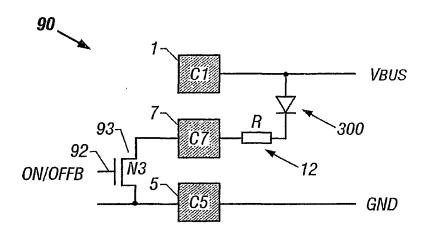


FIG. 12

nunnonin, aun nonnerta L.

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization International Bureau





(43) International Publication Date 21 March 2002 (21.03.2002)

PCT

(10) International Publication Number WO 02/023357 A3

- (51) International Patent Classification⁷: G07F 7/10, G06F 13/42
- G06F 13/38,
- (21) International Application Number: PCT/US01/28311
- (22) International Filing Date:

10 September 2001 (10.09.2001)

(25) Filing Language:

English

(26) Publication Language:

English

(30) Priority Data: 09/661,748

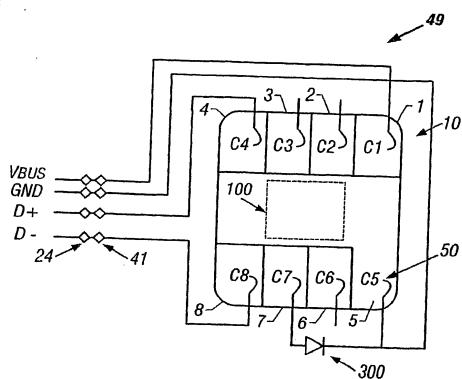
14 September 2000 (14.09.2000) US

- (71) Applicant: STMICROELECTRONICS [US/US]; 1310 Electronics Drive, Carrollton, TX 75006 (US).
- (71) Applicant (for FR only): SCHLUMBERGER SYSTEMES SA [FR/FR]; 50, Avenue Jean-Jaures, F-92120 Montrouge (FR).

- (71) Applicant (for all designated States except FR): SCHLUMBERGER TECHNOLOGIES OPERATING LTD. [GB/GB]; Brewley Building, P.O. Box 3359, BVI Road Town, Tortola (VG).
- (72) Inventors: LEAMING, Taylor, J.; 3217 Silkgrass Bend, Austin, TX 78748 (US). LEYDIER, Robert, Antoine; La Bastide, Quartier les Pichaud, F-83250 La Londe les Maures (FR).
- (74) Agent: RUSSELL, Douglas, D.; Taylor Russell & Russell, P.C., 4807 Spicewood Springs Road, Building One, Suite 1200, Austin, TX 78759-8444 (US).
- (81) Designated States (national): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, PH, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, TZ, UA, UG, UZ, VN, YU, ZA, ZW.

[Continued on next page]

(54) Title: METHOD AND DEVICE FOR SIGNALING DATA TRANSFER BETWEEN A USB PORT AND Λ USB SMART CARD MODULE



(57) Abstract: A method and device are disclosed for detecting successful transfers between a Universal Serial Bus (USB) port and a USB smart card generating a signal that provides an indication of the USB transaction activity. USB transaction activity signal is modulated according to the USB transaction activity and drives a Light Emitting Diode (LED) in a preferred embodiment of the invention. A counter internal to the USB smart card scales the transaction activity signal such that it is perceptible to the user. Because the current through the LED depends upon the USB transaction activity, the brightness of the LED varies according to the USB transaction activity. The LED may be driven from a current mirror sink or source, or a current switch sink or source.

WO 02/023357



- (84) Designated States (regional): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).
- (88) Date of publication of the international search report: 3 April 2003

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

Published:

-- with international search report

INTERNATIONAL SEARCH REPORT

I Application No PCT/US 01/28311

CLASSIFICATION OF SUBJECT MATTER PC 7 G06F13/38 G07F G07F7/10 G06F13/42 According to International Patent Classification (IPC) or to both national classification and IPC **B. FIELDS SEARCHED** Minimum documentation searched (classification system followed by classification symbols) IPC 7 G06F GO7F G06K Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Electronic data base consulted during the International search (name of data base and, where practical, search terms used) EPO-Internal, WPI Data, PAJ C. DOCUMENTS CONSIDERED TO BE RELEVANT Citation of document, with indication, where appropriate, of the relevant passages Category ° Relevant to claim No. Α US 6 119 194 A (BUNNELL JAMES ET AL) 1,9,19 12 September 2000 (2000-09-12) claims 1,9,16,19; figure 5 EP 1 001 329 A (ALADDIN KNOWLEDGE SYSTEMS Α 1,9,19 LTD) 17 May 2000 (2000-05-17) column 5, line 57 -column 7, line 9 column 8, line 57 -column 9, line 28 figures 2,4 WO 00 42491 A (RAINBOW TECHNOLOGIES INC) A 1,9,19 20 July 2000 (2000-07-20) page 10, line 28 -page 11, line 10 page 11, line 25 - line 30 page 28, line 10 -page 29, line 2 figures 1,2,6 Further documents are listed in the continuation of box C. Patent family members are listed in annex. Special categories of clted documents : "T" later document published after the international filing date 'A' document defining the general state of the art which is not considered to be of particular relevance or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention "E" earlier document but published on or after the international "X" document of particular relevance; the claimed invention filling date 'L' document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such docu-"O" document referring to an oral disclosure, use, exhibition or ments, such combination being obvious to a person skilled document published prior to the international filing date but later than the priority date claimed *&* document member of the same patent family Date of the actual completion of the international search Date of mailing of the international search report 12 November 2002 19/11/2002 Name and mailing address of the ISA Authorized officer European Patent Office, P.B. 5818 Patentlaan 2 NL – 2280 HV Rijswijk Tel. (+31-70) 340-2040, Tx. 31 651 epo nl, Fax: (+31-70) 340-3016 Paraf, E

INTERNATIONAL SEARCH REPORT

Inter Application No PCT/US 01/28311

.(Continu	INION) DOCUMENTS CONSIDERED TO BE RELEVANT	
alegory °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
\	FR 2 783 336 A (SCHLUMBERGER IND SA) 17 March 2000 (2000-03-17) claims 1,3,4; figures 1-3	1,9,19
	PATENT ABSTRACTS OF JAPAN vol. 1999, no. 08, 30 June 1999 (1999-06-30) & JP 11 088381 A (NEC CORP), 30 March 1999 (1999-03-30) abstract	1,9,19

Form PCT/ISA/210 (continuation of second sheet) (July 1992)

INTERNATIONAL SEARCH REPORT

Inter d Application No-PCT/US 01/28311

	····	·····				01/20311
Patent docume cited in search re		Publication date		Patent family member(s)		Publication date
US 6119194	Α	12-09-2000	NONE	,		
EP 1001329	Α	17-05-2000	CN EP JP	1262485 1001329 2000200248	A2	09-08-2000 17-05-2000 18-07-2000
WO 0042491	A	20-07-2000	AU EP WO US US	2608200 1141806 0042491 2001043702 2002046342	A1 A1 A1	01-08-2000 10-10-2001 20-07-2000 22-11-2001 18-04-2002
FR 2783336	A	17-03-2000	FR CN EP WO JP	2783336 1317123 1110173 0016255 2002525720	T " A1 A1	17-03-2000 10-10-2001 27-06-2001 23-03-2000 13-08-2002
JP 1108838	1 A	30-03-1999	JP	3134821	B2	13-02-2001

THIS PAGE BLANK (USPTO)

This Page is Inserted by IFW Indexing and Scanning Operations and is not part of the Official Record

BEST AVAILABLE IMAGES

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images include but are not limited to the items checked:
☐ BLACK BORDERS
☐ IMAGE CUT OFF AT TOP, BOTTOM OR SIDES
☐ FADED TEXT OR DRAWING
☐ BLURRED OR ILLEGIBLE TEXT OR DRAWING
☐ SKEWED/SLANTED IMAGES
COLOR OR BLACK AND WHITE PHOTOGRAPHS
GRAY SCALE DOCUMENTS
LINES OR MARKS ON ORIGINAL DOCUMENT
☐ REFERENCE(S) OR EXHIBIT(S) SUBMITTED ARE POOR QUALITY
OTHER:

IMAGES ARE BEST AVAILABLE COPY.

As rescanning these documents will not correct the image problems checked, please do not report these problems to the IFW Image Problem Mailbox.

